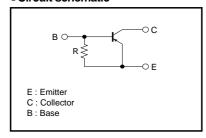
Digital transistors (built-in resistor) DTA144GUA / DTA144GKA

Features

- 1) The built-in bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 2) Only the on / off conditions need to be set for operation, making device design easy.
- 3) Higher mounting densities can be achieved.

●Circuit schematic



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	-50	V	
Collector-emitter voltage	VCEO	-50	V	
Emitter-base voltage	Vево	-5	V	
Collector current	lc	-100	mA	
Collector power dissipation	Pc	200	mW	
Junction temperature	Tj	150	ొ	
Storage temperature	Tstg	-55 to +150	ొ	

Package, marking, and packaging specifications

Part No.	DTA144GUA	DTA144GKA	
Package	UMT3	SMT3	
Marking	K16	K16	
Packaging code	T106	T146	
Basic ordering unit (pieces)	3000	3000	

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	_	-	V	Ic= -50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВУево	-5	-	-	V	I _E = -160μA
Collector cutoff current	Ісво	-	_	-0.5	μΑ	Vcb= -50V
Emitter cutoff current	ІЕВО	-65	_	-130	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	_	_	-0.3	V	Ic= -10mA , I _B = -0.5mA
DC current transfer ratio	hfe	68	-	-	-	Ic=-5mA , Vc=-5V
Emitter-base resistance	R	32.9	47	61.1	kΩ	-
Transition frequency	f⊤	_	250	_	MHz	Vc=-10V, Ie=5mA, f=100MHz *

^{*} Transition frequency of the device.

•Electrical characteristics curves

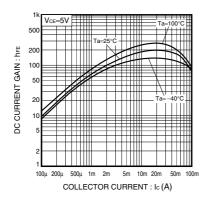


Fig.1 DC current gain vs. Collector current

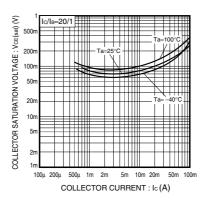


Fig.2 Collector-Emitter saturation voltage vs. Collector current

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